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Patent Abstracts of Japan

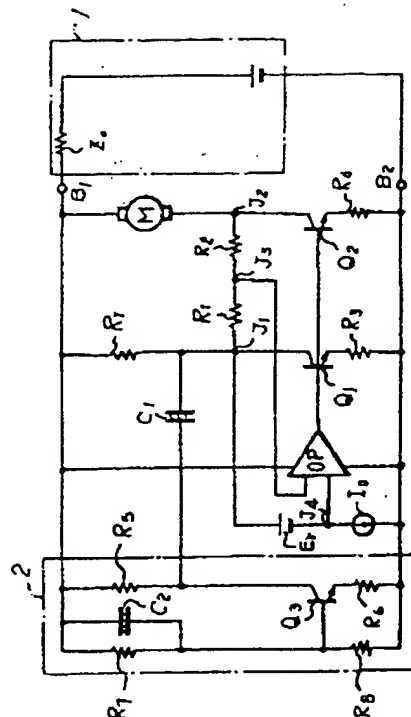
PUBLICATION NUMBER : JP59017894
 PUBLICATION DATE : 30-01-84
 APPLICATION NUMBER : JP820125472
 APPLICATION DATE : 19-07-82

VOL: 8 NO: 103 (E - 244)
 AB. DATE : 15-05-1984 PAT: A 59017894
 PATENTEE : PIONEER KK
 PATENT DATE: 30-01-1984

INVENTOR : ISHIZUKA KAZUNORI

INT.CL. : H02P7/28

TITLE : CONTROL DEVICE FOR
 PROPORTIONAL CURRENT CONTROL
 TYPE DCMOTOR



ABSTRACT : PURPOSE: To suppress the ripple component for a power source by superposing the ripple component of the power source inverted in phase on the voltage drop due to proportional resistor.
 CONSTITUTION: An inverting amplifier 2 inverts the phase, amplifies and outputs the ripple components produced at the power supply terminal B1 due to the internal impedance Z0 of a DC power source 1 and the load variation in the DC motor M. This output voltage is supplied through an AC coupling capacitor C1 to the terminal J1 of a proportional resistor RT. An operational amplifier OP controls transistors Q1, Q2 in response to the deviation between the potential difference between the terminal J1 and J2 varying in response to the variation in the load of the DC motor and a reference voltage. In this manner, the ripple voltage capable of being produced on power supply terminal B1 can be sufficiently suppressed.